ABOUT THE INSTITUTION

Velalar College of Engineering and Technology is a self-financing co-educational Autonomous institution established in the year 2001 with all the necessary infrastructural facilities provided by Vellalar Educational Trust that ably governs the institution. The college is approved by AICTE, affiliated to Anna University, Chennai and accredited with 'A' Grade by NAAC. The BE programmes of ECE, EEE, CSE & BME have been accredited by NBA. The institution has completed 15 years of dedicated and excellent service in the field of technical education. The institution offers seven UG Programme (BME, Civil Engg., CSE, ECE, EEE, IT, & Mech. Engg.) and six PG (M.E - CSE, Embedded System Technologies, Applied Electronics, VLSI Design, MBA & MCA) programmes. There are five research centers namely EEE, ECE, Management Studies, Physics and Chemistry which have been approved by Anna University.

The Vision of this college is to provide a wide range of academic and research programmes and strive to develop a world class center of excellence for learning. The college has signed Memorandum of Understanding [MoU] with many leading corporates and Multi-National companies to enable the faculty and students to gain practical knowledge and exposure to industrial environments. An Industry - Institute Partnership Cell and Entrepreneurship Development Cell have been established to expose scholars to the industrial sectors and these are supported by funds from AICTE.

ABOUT THE DEPARTMENT

The Department of ECE has well equipped laboratories, well established computer centers with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Networking, Microprocessors and Microcontrollers, DSP, VLSI Design, Embedded Systems, RF and Microwave, Satellite and Optical & Wireless Communications. The department offers a BE programme in ECE and ME programmes in Applied Electronics and VLSI Design. The department has two industry sponsored R&D centre.

ABOUT THE HANDS-ON TRAINING

The primary objective of this workshop is:

- To demonstrate an low cost alternative to the proprietary software
- To create awareness among academic community about availability of open source EDA tool for carrying out RTL-GDSII flow.
- To demonstrate ASIC design flow from RTL to GDSII using open source EDA
- To demonstrate design of analog and mixed signal circuits using open source EDA

COURSE CONTENTS

- ✓ **ASIC Design Flow using open Source Platform**
- ✓ **Analog IC Design – Schematic Capture, Layout Design and Optimization**
- ✓ **NanoSim**

THREE DAYS HANDS-ON TRAINING ON
"VLSI DESIGN USING OPEN SOURCE EDA TOOL-AARON"
REGISTRATION FORM

14th – 16th December 2016

Co-ordinators
Ms. M.Parimaladevi
Ms. P.Brindha

Organized by

DEPARTMENT OF ECE
(Accredited by NBA, New Delhi)

VELALAR COLLEGE OF ENGINEERING AND TECHNOLOGY (An Autonomous Institution)

Accredited by NAAC with 'A' grade

Thindal, ERODE - 638012, Tamil Nadu.
Ph: 0424-2244201 to 2244204 & 2244206
Fax: 0424-2244205

Website: www.velalarengg.ac.in

THREE DAYS HANDS-ON TRAINING ON
"VLSI DESIGN USING OPEN SOURCE EDA TOOL-AARON"
REGISTRATION FORM

14th – 16th December 2016

1. Name   :
2. Designation   :
3. Department   :
4. Institution   :
5. Experience   :
6. Address   :

7. Mobile   :
8. E-mail   :
9. Accommodation Required : Yes/No

Signature of the applicant

Dr./Mr./Ms…………………………………
is an employee of our Institution/ Research Scholar and is hereby permitted to participate in the three days hands-on training on "VLSI Design Using Open Source EDA Tool-AARON" at Velalar College of Engineering and Technology.

Signature of Head of the Department
(with the official seal)

Place:
Date :

ABOUT REGISTRATION

- Number of participants is restricted to 35.
- Selection on first come first serve basis.
- Preference will be given to Research Scholars.
- Selected candidates will be intimated through E-mail.
- Confirmation of participation is to be made by E-mail.
- Registration Fees:
  - **Faculty/Research Scholars : Rs.750/-**
  - **PG students   : Rs.500/-**
- No TA/DA will be paid.
- Send the duly filled in registration form to reach the coordinators by speed post / courier.

ACCOMMODATION

Accommodation for the participants may be arranged in the hostel on request (Rs.200/day).

HOW TO REACH ?

The college is situated at Thindal, which is 8 km from Erode railway station and Erode bus stand, on the Erode to Perundurai state Highway.

RESOURCE PERSONS:

The sessions will be handled by experts from **Sanaris Technologies, Bangalore.**

ADDRESS FOR COMMUNICATION
Ms.M.Parimaladevi,
Ms.P.Brindha,
Department of ECE,
Velalar College of Engineering and Technology,
Thindal, Erode - 638 012, Tamil Nadu.
E-mail: **brindhaten@gmail.com**
Mobile no: **9488470240, 9952714387**

ORGANIZING COMMITTEE:

CHIEF PATRON

Thiru. S. S. Kandasamy,

President , Vellalar Educational Trust

PATRON

Thiru. S. D. Chandrasekar,

Secretary & Correspondent

Velalar College of Engineering and Technology

PRESIDENT

Dr. M.Jayaraman, Principal

Velalar College of Engineering and Technology

VICE PRESIDENTS

Prof. L. Peter Stanley Bebington, Dean (A)

Prof. P. Jayachandar, Dean (SA)

CONVENER

Dr. K.R.Valluvan, HOD/ECE

COORDINATORS

Ms.M.Parimaladevi, AP (Sr.Gr.)/ECE

Ms.P.Brindha, AP (Sr.Gr.)/ECE,

IMPORTANT DATES:

Submission of Application	:**07.12.2016**
Intimation of Selection	:**10.12.2016**
Confirmation by Participants	:**12.12.2016**