

Velalar College of Engineering and Technology

(Autonomous)

Affiliated to Anna University, Chennai, Accredited by NAAC with 'A' Grade,
Thindal, Erode, Tamilnadu - 638012.



Department of Electronics and Communication Engineering

(Accredited by NBA)

Organizes

AICTE sponsored one week STTP on

Effective and Innovative Teaching, Best Practices and Learning Strategies

August 17-22, 2020 (Series-I)

September 21-26, 2020 (Series-II)

October 19-24, 2020 (Series-III)



ABOUT THE INSTITUTION

Velalar College of Engineering and Technology, Erode is a self-financing co-educational institution established in the year 2001 with all the necessary infrastructural facilities provided by Vellalar Educational Trust that ably governs the institution. The college is approved by the AICTE, New Delhi, Affiliated to Anna University, Chennai and Accredited with 'A' Grade by NAAC. The UG programmes of ECE, EEE, CSE, BME & IT are accredited by NBA. The institution has completed 18 years of dedicated and excellent service in the field of technical education. Our institution offers eight UG programmes (BME, Civil Engg., CSE, ECE, EEE, IT, Mech Engg., Medical Electronics) and six PG programmes (M.E - CSE, Applied Electronics, Embedded System Technologies, VLSI Design, MCA, MBA) . There are five Anna University approved Research centers in the department of CSE, ECE, EEE, Physics and Chemistry.

The Vision of the institution is to provide a wide range of academic and research programmes and strive to develop a world class center of excellence for learning. The college has signed Memorandum of Understanding [MoU] with many leading corporate and Multinational companies to enable the faculty and students to gain practical knowledge and exposure to industrial environments. An Industry - Institute Partnership Cell and Entrepreneurship Development Cell have been established to expose students to the industrial sectors, are supported by funds from AICTE. Funds have been received from various agencies like CSIR, DST, ICMR, DRDO and ISRO.



ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was started in the year 2001. The department offers B.E. Programme in Electronics and Communication Engineering, M.E. Programme in Applied Electronics and VLSI Design and is recognized as research center to offer Ph.D. programme. Our department faculty has been granted 2 patents in this year. 4 patents have been published. It has good infrastructural amenities which include state of the art laboratories; approved research supervisors and dedicated faculty members covering diverse specialization: Communication, VLSI Design, Embedded systems and Electronics. The department is vibrant with several activities round the year. To meet the required academic standards, the quality instruction to students is imparted by the qualified faculty members providing good academic insights. The research expertise of the department shelters Networking, Microprocessors, DSP, Microcontrollers, VLSI Design, Embedded System, Microwave, RF, Satellite, Optical & Wireless Communications.

VISION

To mould the electronics and communication engineering students to become world class engineers for the benefit of the society and further pioneer themselves to make use of the recent inventions coming out from laboratories then and there.

MISSION

Training the electronics and communication engineering students as knowledge based engineers, scientists, technocrats, administrators, entrepreneurs and academicians with social and professional ethics and leadership qualities to meet the challenges in electronics and communication engineering field by logical and innovative thinking for the betterment of mankind.

ABOUT THE STTP

This program aims to inculcate the knowledge on modern ICT tools and their usage, Innovative and best practices for teaching-learning process, Improving the Research Impact, initiatives for Higher Learning, Pedagogic excellence, Digital teaching resources and Teaching strategies to facilitate Active Learning. The COVID-19 has resulted in schools and colleges shut all across the world. As a result, education has changed dramatically, with the distinctive rise of e-learning, whereby teaching is undertaken remotely and on digital platforms. With this sudden shift away from the classroom in many parts of the globe, this AICTE sponsored STTP may help the faculty for the adoption of online learning to persist post-pandemic, and how such a shift would impact the worldwide education market. The STTP is planned to be conducted in three series from August to October months with 6 days in each series.

TOPICS LIKELY TO COVER

- Outcome Based Education (OBE)
- Online teaching strategies
- MHRD's initiatives for Higher Learning
- Pedagogic excellence - how to be a better teacher
- Innovative Practices in Curriculum Design
- Creative Teaching and Learning strategies
- Digital teaching resources
- Improving the Research Impact

4 sessions
per day

9.30 am - 11:00 am

11:15 am - 12:45 pm

01.45pm - 3.15 pm

03:30 pm - 05:00 pm

**AICTE sponsored one week STTP on
Effective and innovative Teaching, Best practices and learning strategies**

Day - 1 - 21.09.2020

Session 1

9.30 a.m. to 11.00 a.m



Dr. N. K. KARTHIKEYAN
PROFESSOR & HEAD OF THE DEPARTMENT
DEPARTMENT OF INFORMATION TECHNOLOGY
COIMBATORE INSTITUTE OF TECHNOLOGY
COIMBATORE - 641 014

Session 2

11.15 a.m. to 12.45 p.m



Dr. M. JAYARAMAN
PRINCIPAL & PROFESSOR
DEPARTMENT OF MECHANICAL ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012

Session 3

1.45 p.m. to 3.15 p.m



Patentee. L. PETER STANLEY BEBINGTON
PROFESSOR & DEAN(ACADEMIC)
VELALAR COLLEGE OF ENGINEERING
AND TECHNOLOGY
ERODE-638012

Session 4

3.30 p.m. to 5.00 p.m



Dr. V.K.MANAVALASUNDARAM
PROFESSOR & HOD
DEPARTMENT OF INFORMATION
TECHNOLOGY
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012

**AICTE sponsored one week STTP on
Effective and innovative Teaching, Best practices and learning strategies**

Day - 2 - 22.09.2020

Session 1

9.30 a.m. to 11.00 a.m



Dr. C. KAMALANATHAN
ASSOCIATE PROFESSOR,
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
GITAM SCHOOL OF TECHNOLOGY
GITAM UNIVERSITY, BENGALURU

Session 2

11.15 a.m. to 12.45 p.m



Dr. D. AARTHI
ASSISTANT PROFESSOR (Sr.Gr.)
DEPARTMENT OF MASTER OF BUSINESS
ADMINISTRATION
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY
ERODE-638012

Session 3

1.45 p.m. to 3.15 p.m



Dr. K.R. ANANTH
PROFESSOR
DEPARTMENT OF MASTER OF COMPUTER
APPLICATIONS
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY
ERODE-638012

Session 4

3.30 p.m. to 5.00 p.m



Dr. V. CHANDRASEKARAN
PROFESSOR AND HEAD
DEPARTMENT OF MEDICAL ELECTRONICS
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY
ERODE - 638012

**AICTE sponsored one week STTP on
Effective and innovative Teaching, Best practices and learning strategies**

Day - 3 - 23.09.2020

Session 1

9.30 a.m. to 11.00 a.m



**Dr. B. MEENAKSHIPRIYA
PROFESSOR & HOD**

**DEPARTMENT OF MECHATRONICS
KONGU ENGINEERING COLLEGE
PERUNDURAI, ERODE-638 060**

Session 2

11.15 a.m. to 12.45 p.m



**Dr. S. JABEEN BEGUM
PROFESSOR & HOD**

**DEPARTMENT OF COMPUTER SCIENCE AND
ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012**

Session 3

1.45 p.m. to 3.15 p.m



**Dr. K.R. VALLUVAN
PROFESSOR & COE**

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012**

Session 4

3.30 p.m. to 5.00 p.m



**Dr. M. NISHA ANGELINE
PROFESSOR**

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING
AND TECHNOLOGY, ERODE-638012**

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Day - 4 - 24.09.2020

Session 1

9.30 a.m. to 11.00 a.m



**Dr.N. MOHAN KUMAR
PROFESSOR**

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
GITAM SCHOOL OF TECHNOLOGY
BENGALURU**

Session 2

11.15 a.m. to 12.45 p.m



**Dr. S. MANGAI
PROFESSOR & HOD**

**DEPARTMENT OF BIOMEDICAL ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY
ERODE-638012**

Session 3

1.45 p.m. to 3.15 p.m



**Dr. S.KALALVIZHI
ASSOCIATE PROFESSOR**

**DEPARTMENT OF COMPUTER SCIENCE
ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012**

Session 4

3.30 p.m. to 5.00 p.m



**Dr.S.MAHENDRAKUMAR
PROFESSOR**

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING
AND TECHNOLOGY, ERODE-638012**

**AICTE sponsored one week STTP on
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Day - 5 - 25.09.2020

Session 1

9.30 a.m. to 11.00 a.m



Dr. M. SREEDHAR

PROFESSOR

**DEPARTMENT OF ELECTRICAL AND
ELECTRONICS ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE - 638012**

Session 2

11.15 a.m. to 12.45 p.m



Dr.S.K.MANIKANDAN

PROFESSOR

**DEPARTMENT OF ELECTRICAL AND
ELECTRONICS ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE - 638012**

Session 3

1.45 p.m. to 3.15 p.m



Dr. M.KALAMANI

PROFESSOR

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE-638012**

Session 4

3.30 p.m. to 5.00 p.m



Dr.S.RAJAN

PROFESSOR

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING
AND TECHNOLOGY, ERODE-638012**

**AICTE sponsored one week STTP on
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Day - 6 - 26 .09.2020

Session 1

9.30 a.m. to 11.00 a.m



**Dr.K.G.PARTHIBAN
PROFESSOR AND HOD
ELECTRICAL SCIENCES
ADITHYA INSTITUTE OF TECHNOLOGY
COIMBATORE**

Session 2

11.15 a.m. to 12.45 p.m



**Dr.K.VENKATACHALAM
PROFESSOR AND HEAD
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
VELALAR COLLEGE OF ENGINEERING AND
TECHNOLOGY, ERODE - 638012**

Session 3

1.45 p.m. to 3.15 p.m



**Dr. G. MARY JANSI RANI
PROFESSOR
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
SRI RAMAKRISHNA ENGINEERING COLLEGE
COIMBATORE - 641 022**

SERIES - II

ELIGIBILITY

Faculty members and research scholars belonging to AICTE approved technical Institutions, Universities can participate in the programme.

Registration Details

- No Registration Fee
- Last date for Registration (Series-I): 16.08.2020
- Last date for Registration (Series-II): 19.09.2020
- Last date for Registration (Series-III): 17.10.2020
- Registration URL: <https://bit.ly/vcetsttpeces2>
- Participants can participate only in any one of the three series
- Details related to registration confirmation and meeting link will be shared through e-mail
- Maximum number of participants is limited, preference will be given on first come first serve basis

Registration QR Code



E-CERTIFICATE

e-Certificate will be issued on meeting the following criteria

- Participant attendance should not be less than 80%
- Participant should submit the daily attendance and feedback form
- Participant should secure minimum 50% of marks in the assessment conducted at the end of the STTP



ORGANIZING COMMITTEE

CHIEF PATRON

Thiru. S.S. Kandasamy
President/Vellalar Educational Trust.

PATRON

Thiru. S.D. Chandrasekar
Secretary/ Vellalar Educational Trust.

PRESIDENT

Dr. M.Jayaraman
Principal/VCET.

VICE PRESIDENT

Patentee. L.Peter Stanley Bebington
Dean - Academic.
Prof. P. Jayachandar
Dean- Student Affairs.

CONVENER

Dr.K.Venkatachalam,
Professor and Head/ECE

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COORDINATORS

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Dr.S.Mahendrakumar,
Professor/ECE
Dr.S.Rajan,
Professor/ECE

