

VELALAR COLLEGE OF ENGINEERING AND TECHNOLOGY,
ERODE - 638012

(Autonomous)

Accredited by NAAC with 'A' Grade



DEPARTMENT OF ECE

Accredited by NBA

ASSOCIATION OF ECE - CENIREA

PRESENTS

One Week Online Value
Added Course



TOPIC :-
DIGITAL ELECTRONICS
FOR
INDUSTRIAL APPLICATIONS

CONVENER

Dr.M.Nisha Angeline
Professor & Head/ECE

CO-ORDINATORS

Mr.G.Boopathi Raja, AP(Sr.Gr.)/ECE
Ms.P.Nandhini, AP(Sr.Gr.)/ECE
Ms.S.Aiswarya, AP/ECE

Platform: Zoom

Date: 02.11.2020 - 08.11.2020

Time : 2.00pm to 5.00 pm

SCHEDULE

DAY 1

Er.D.Karthe

Senior Associate

Cognizant Technology Solutions,Coimbatore.

Topic: Design Thinking in Digital Electronics

What is innovation - Design thinking vs Innovation - Stages in Design thinking - Digital Transformation - Activity.

(02.11.2020)

DAY 2

Ms.V.Aarthi

Assistant Professor(Sr.Gr.)/ECE

VCET, Erode.

Topic: Digital Circuit Design using Proteus

Logic Gates - Adder - Comparator - Flip-Flops - Multiplexer - ADC, DAC - Asynchronous Counter - Traffic Light Control using D flip-flop - Visitor Counter - Electronic Voting Machine - Power plant Automation.

(03.11 2020)

DAY 3

Dr.M.Nisha Angeline

Professor and HOD / ECE

VCET,Erode.

Topic: Role of PLC in Industries

Introduction - Controllers - PLC - Real time examples- Demonstration

(04.11.2020)

Dr.M.Nisha Angeline

Professor and HOD / ECE
VCET,Erode.

Topic: Modeling of Digital Logic Circuits using Verilog HDL

*Operators - Variables - Conditional Statements - Dataflow Modeling
- Behavioral Modeling - Structural Modeling - Logic gates -
Combinational Circuits - Flip-flops - Counters - Registers.*

DAY 4

(05.10.2020)

Ms.M.Shivaranjani

Assistant Professor/ECE
VCET,Erode.

Topic: Discussion on Previously asked Competitive Exam Questions related with Digital Electronics

DAY 5

(6.11.2020)

Er.B.Rajesh Kumar

2nd Engineer
TCI Seaways,
Transport Corporation of India, Chennai.

Topic: Role of Electronics in Future Industries & Career Guidance

DAY 6

(07.11.2020)

DAY 7

(08.11.2020)

Assessment