

ABOUT THE INSTITUTION

Velalar College of Engineering and Technology (VCET) established in the year 2001, approved by AICTE, New Delhi and affiliated to Anna University, Chennai. It is governed by Vellalar Educational Trust (VET). It has completed 16 years of dedicated and excellent service in technical education. The college offers 7 Under-graduate programmes, 6 Post-graduate programmes and 5 Research programmes. The college has been accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade. The college has attained the autonomous status in the year 2016.

ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was established during the year academic 2001-2002. It offers UG degree in Electrical and Electronics Engineering and PG degree in Embedded System Technologies. From the inception, the department has been playing a pioneering role in producing world class technocrats.

LOCATION

VCET is located on Erode-Perundurai road opposite to Thindal Murugan Temple about 7km from Erode bus stand and railway station.

ABOUT THE FDTP

The primary objective of the faculty development training programme is to provide opportunity for the faculty members from various engineering colleges to enrich their knowledge in the field of "**EMBEDDED SYSTEMS**". This will equip the faculty to teach the subject in better way and also enable them to offer research and innovative ideas in this specialization.

COURSE CONTENTS

- Introduction to Embedded System
- Embedded Networking
- Embedded Firmware Development Environment
- RTOS based Embedded System Design
- Case Studies in Embedded Systems

ABOUT REGISTRATION

- Number of Participants is restricted to 25
- Selection on first come first serve basis
- Preference will be given to faculty based on eligibility criteria
- Selected candidate will be intimated through e-mail
- Confirmation of participation is to be made by e-mail
- No TA/DA will be paid
- No registration fee

Send the duly filled in registration form to reach the co-ordinator by speed post/courier on or before 22.11.17.

SEVEN DAYS FDTP ON EE6602 - EMBEDDED SYSTEMS

8-15th December, 2017
REGISTRATION FORM

COORDINATORS

Dr. S. K. MANIKANDAN Assoc.Prof/EEE
Mr. K. RAJARAM AP(Sr.Gr)/EEE



Approved by
Anna University, Chennai

Organized by



Department of Electrical and Electronics
Engineering
(Accredited by NBA, New Delhi)
Velalar College of Engineering & Technology
(AUTONOMOUS)
(Accredited by NAAC with 'A' Grade)
Thindal, Erode-12.
Ph: 0424-2244201/02
Fax: 0424-2244205
www.velalarengg.ac.in

REGISTRATION FORM

**ANNA UNIVERSITY APPROVED
SEVEN DAYS FDTP ON
EE6602 - EMBEDDED SYSTEMS
8-15th December, 2017**

1. Name :
2. Designation :
3. Educational Qualification:
4. Institution :
5. Address for communication:
6. Mobile :
7. E-mail :
8. Accommodation
Required : YES NO

I hereby declare that the details furnished above are true to the best of my knowledge and belief.

Place:

Date:

Signature of the Participant

**CERTIFICATE FROM
HEAD OF THE INSTITUTION**

Dr/Mr./Ms/_____ of
_____ department is a faculty of our
institution. He / She is permitted to attend the
FDTP on “**EE6602-Embedded Systems**” during
the period 8.12.2017 to 15.12.2017.

Signature of the Principal with seal

ADDRESS FOR CORRESPONDENCE

The Coordinator,
FDTP on EMBEDDED SYSTEMS,
Department of Electrical and Electronics Engg,
Velalar College of Engineering & Technology,
Thindal, Erode-638012.

E- Mail : drmani0705@gmail.com
rajaramacademic@gmail.com

Mobile no : +91 7010747425
+91 9488868844

ORGANIZING COMMITTEE

- CHIEF PATRON:** **Vice-Chancellor**
Anna University,
Chennai.
- PATRON:** **Dr.S.Ganesan**
Registrar,
Anna University,
Chennai.
- CHAIR:** **Dr.K.Shanthi**
Director, CFD.
- CO-CHAIR:** **Dr.D.Sridharan**
Addl. Director, CFD
- Dr.M.Jayaraman**
Principal, VCET.
- Dr.P.Sakthivel**
HOD / EEE, VCET.
- Coordinators:** **Dr.S.K.Manikandan**
Assoc. Prof. / EEE
- Mr.K.Rajaram**
Asst Prof. / EEE.
- Venue: Conference Hall, Second Floor,
VCET.**

IMPORTANT DATES:

- Submission of Application : 22.11.2017
Intimation of selection : 25.11.2017
Confirmation by Participants : 30.11.2017